## AMENDEMENTS TO THE SPECIFICATION

Please replace paragraph [0001] with the following amended paragraph:

[0001]    Related subject matter is found in my copending U.S. Patent Application No. 10/626,063, [[_____,]] entitled "Communication Terminal with Low Carrier Feedthrough and Communication System Using Such a Terminal", filed of even date herewith and assigned to the assignee hereof.

Please replace paragraph [0038] with the following amended paragraph:

[0038]    Receive path 730 includes performs the reverse process as transmit path 720 and includes corresponding blocks to those in transmit path 720, including an RF receiver and downconverter 732, a line decoder demodulator 734, and a channel decoder forward error decoding block 736.    RF receiver and downconverter 732 has an input terminal connected to antenna 740, and I and Q output terminals.    RF receiver and downconverter 732 converts the received RF signal to a baseband signal formed by signals I and Q.    Many suitable RF receiver architectures are known to those in the art, including a direct downconverter and a downconverter that first converts the received signal to IF for further filtering before converting the IF signal to baseband.    Demodulator 734 has input terminal connected to the I and Q output terminals of RF receiver and downconverter 732, and an output terminal.    Demodulator 734 performs the reverse modulation process as modulator 724.    Forward error decoding block 736 has an input terminal connected to the output terminal of demodulator 734, and an output terminal for providing an output signal of modem 710 labeled "DATA OUT".    Forward error decoding block 736 performs the reverse process as forward error coding block 722.    For example if convolutional coding was used, forward error decoding block 736 performs decoding using the Viterbi algorithm to decode the symbols encoded on the I and Q signals into a stream of data bits.